



**P/N: H48001J**

- \* Designed to meet IEEE802.3an requirement
- \* Complying with RoHS&Halogen Free requirements
- \* Designed to meet IR 255°C peak Requirement
- \* Primary inductance 100μH min with 0mA DC Bias
- \* Operating temperature range:-40°C to +90°C
- \* Storage temperature range:-40°C to +90°C
- \* POE applications with 720mA current capability

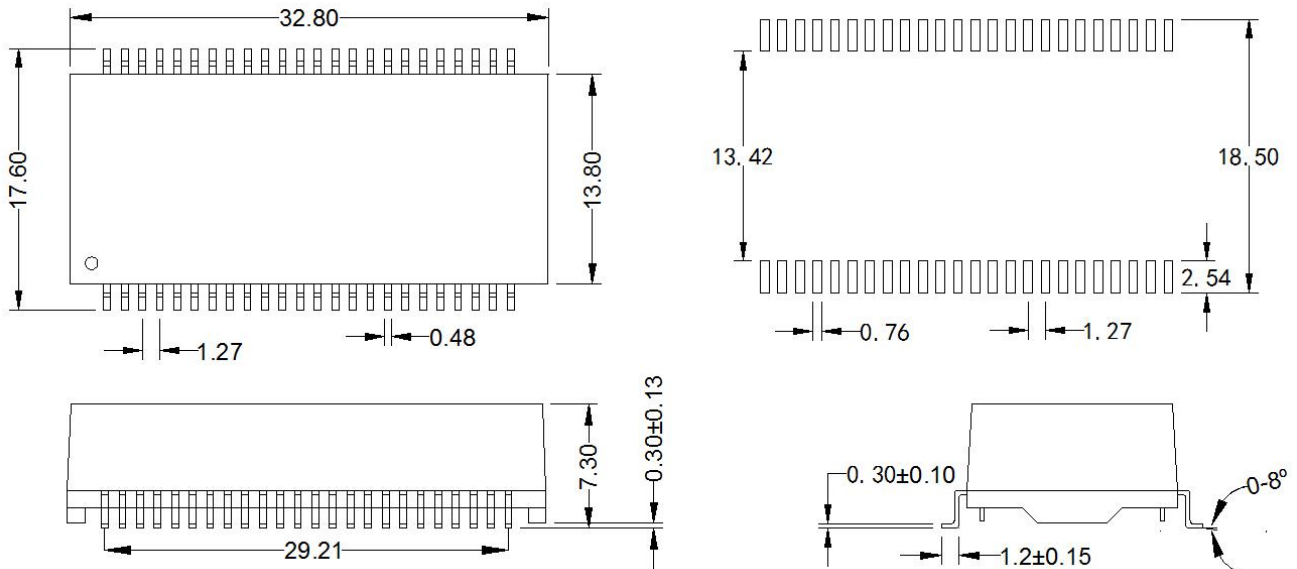
**Electrical Specifications@25° C**

Part Number	Turn Ratio (±3%)	OCL(uH Min) @ 100KHz/0.1V With 0mA DC Bias	L.L (uH Max)	C <sub>w/w</sub> (pF Max)	DCR (Ω Max)	Hi-Pot (Vrms MIN)
H48001J	1CT:1CT	100	0.2	28	0.7	AC1500 / 1mA 1S

**Continue**

Part Number	Insertion Loss (dB Max)			Return Loss (dB Min)			Cross Talk (dB Min)		DCMR (dB Min)	
	1-100MHz	400MHz	500MHz	0.3-40MHz	100- 400MHz	400- 500MHz	1-100MHz	100-500MHz	1-250MHz	250-500MHz
H48001J	-1.0	-1.5	-2.0	-18	-18+10Log <sub>10</sub> (f/40)	-8+30Log <sub>10</sub> (f/400)	-40	-30	-30	-22

**Mechanical**



Dimensions: mm  
 Unless otherwise specified tolerances: xx.xo ± 0.25mm  
 o.xx ± 0.05mm

**Schematic**

